General purpose transistors (dual transistors)

EMT18 / UMT18N / IMT18

Features

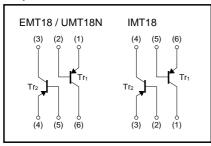
- 1) Two 2SA2018 chips in a EMT package.
- 2) Mounting possible with EMT3 or UMT3 or SMT3 automatic mounting machines.
- Transistor elements are independent, eliminating interference.

Structure

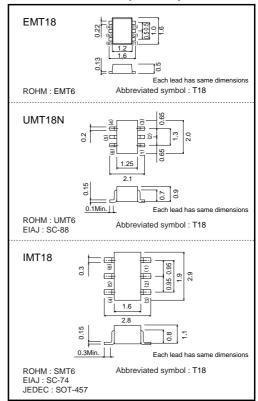
Epitaxial planar type NPN silicon transistor

The following characteristics apply to both Tr₁ and Tr₂.

●Equivalent circuit



●External dimensions (Unit : mm)



● Absolute maximum ratings (Ta=25°C)

<u> </u>							
Parameter	Symbol		Limits	Unit			
Collector-base voltage	V _{CBO}		-15	V			
Collector-emitter voltage	V _{CEO}		-12	V			
Emitter-base voltage	V _{EBO}		-6	V			
Collector current	lc		-500	mA			
	ICP		1.0 *1	Α			
		EMT6	150 (TOTAL)*2	mW			
Power dissipation	Pc	UMT6	150 (TOTAL)				
		SMT6	300 (TOTAL)*3				
Junction temperature	Tj		150	°C			
Storage temperature	Tstg		-55 to +150	°C			

- *1 Single pulse Pw=1ms
- *2 120mW per element must not be exceeded.
- *3 200mW per element must not be exceeded

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Collector-base breakdown voltage	ВУсво	-15	_	_	٧	I _C = -10μA	
Collector-emitter breakdown voltage	BV _{CEO}	-12	_	_	V	I _C = -1mA	
Emitter-base breakdown voltage	ВУево	-6	_	_	V	I _E = -10μA	
Collector cutoff current	Ісво	_	_	-0.1	μΑ	V _{CB} = -15V	
Emitter cutoff current	ІЕВО	_	_	-0.1	μΑ	Vcb=-6V	
Collector-emitter saturation voltage	V _{CE} (sat)	_	-100	-250	mV	I _C / I _B = -200mA / -10mA	
DC current transfer ratio	hfe	270	_	680	-	Vce= -2V, Ic= -10mA	
Transition frequency	f⊤	_	260	_	MHz	V _{CE} = -2V, I _E =10mA, f=100MHz	
Output capacitance	Cob	_	6.5	_	pF	V _{CB} = -10V, I _E =0A, f=1MHz	

●Packaging specifications and hFE

	Package name		Taping	
Type	Code	T2R	TR	T110
	Basic ordering unit (pieces)	8000	3000	3000
EMT18		0	-	_
UMT18N		-	0	_
IMT18		İ	_	0

•Electrical characteristic curves

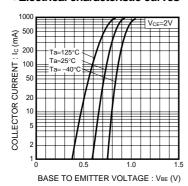


Fig.1 Grounded Emitter Propagation Characteristics

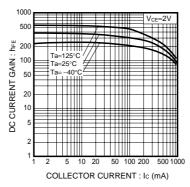


Fig.2 DC Current Gain vs. Collector Current

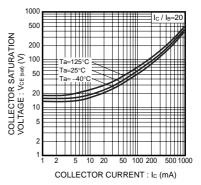


Fig.3 Collector-Emitter Saturation Voltage vs. Collector Current (I)

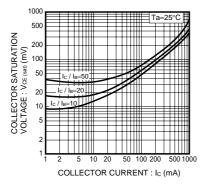


Fig.4 Collector-Emitter Saturation Voltage vs. Collector Current (II)

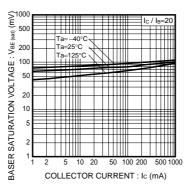


Fig.5 Base-Emitter Saturation Voltage vs.Collecter Current

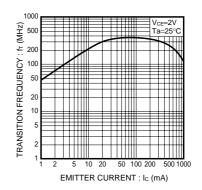


Fig.6 Gain Bandwidth Product vs. Emitter Current

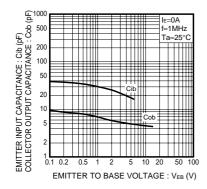


Fig.7 Collector Output Capacitance vs. Collector-Base Voltage Emitter Input Capacitance vs. Emitter-Base Voltage

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